

#### INVENTOR INFORMATION

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#### CORRESPONDENCE INFORMATION

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#### APPLICATION INFORMATION

Title Line One:: SEMICONDUCTOR INTEGRATED CIRCUIT TESTING  
Title Line Two:: SYSTEM AND METHOD  
Total Drawing Sheets:: 9  
Formal Drawings?: Yes  
Application Type:: Utility  
Docket Number:: 740819-653  
Secrecy Order in Parent Appl.?: No

#### REPRESENTATIVE INFORMATION

Representative Customer Number:: 22204  
Registration Number One:: 38285  
Registration Number Two:: 28290  
Registration Number Three:: 35483  
Registration Number Four:: 27997  
Registration Number Five:: 45285

#### PRIOR FOREIGN APPLICATIONS

Foreign Application One:: 2001-028597  
Filing Date:: 02-05-2001  
Country:: Japan

Priority Claimed:: No

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